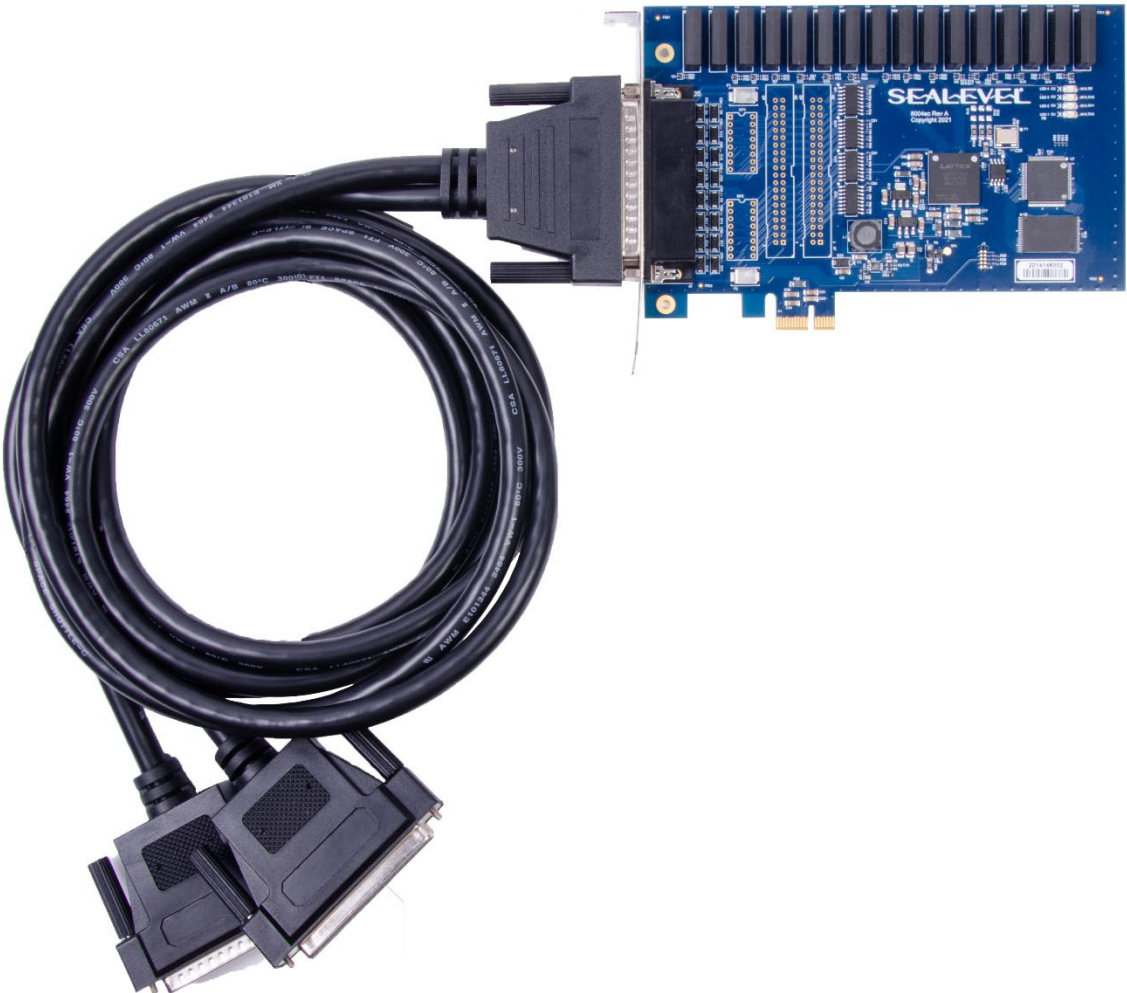


DIO-32.PCLe

User Manual | 8004ec & 8004Hec



SEALEVEL®

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Before You Get Started

What's Included

The 8004ec is shipped with the following items. If any of these items are missing or damaged, please contact Sealevel for replacement.

- **8004ec - DIO-32.PCie Digital I/O Adapter**
 - **8004ec – Board with SMT resistors - 3-30V inputs**
 - **8004Hec – Board with Socketed resistors – User Configurable Input Range (default 3-30V)**
- **CA165 - DB78 Male to D37 Male and DB37 Female Y-cable**

Advisory Conventions



Warning

The highest level of importance used to stress a condition where damage could result to the product, or the user could suffer serious injury.



Important

The middle level of importance used to highlight information that might not seem obvious or a situation that could cause the product to fail.



Note

The lowest level of importance used to provide background information, additional tips, or other non-critical facts that will not affect the use of the product.

Introduction

Overview

The DIO-32.PCIe digital I/O interface provides 16 optically isolated inputs and 16 reed relay SPST (single pole-single throw) outputs. The inputs protect the PC and other sensitive equipment from spikes and ground loop current that can be generated in industrial environments, while the outputs provide high quality, long life, low current (10 Watt maximum), dry contact switch closures. Reed relays are well suited for low current applications. The relays are normally open, and close when energized.

The DIO-32.PCIe is designed to be used with Windows Operating Systems. The Seal/O API (Application Programming Interface) included in Sealevel's software provides a variety of useful high-level function calls implemented as a Windows dynamic link library (.DLL). In addition to the API, Seal/O includes a device driver, sample code, and utilities to simplify software development.

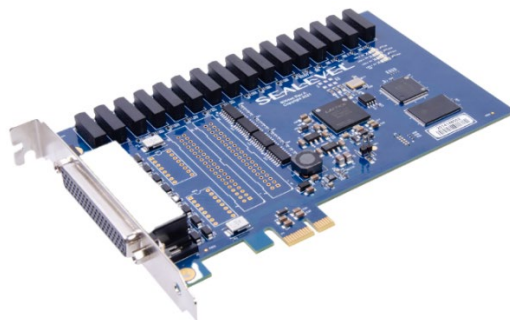
8004ec vs 8004Hec

The 8004ec features surface mount input resistors that give the optically isolated inputs their +3v to +30v input range.

The 8004Hec offers more flexibility as the 8004Hec features socketed DIP resistors which allow the user to configure an input range (up to +36V) based on what socket resistors the user selects. The chosen socketed resistor value will determine what the turn-on voltage will be for the optically isolated inputs in accordance with the [8004Hec Socketed Input Resistor Selection Table](#).

Features




- PCI Express x1 Host Interface
- 16 optically isolated inputs
- 16 Reed relay outputs (SPST)
- Highly reliable 10W / 10VA Reed relays
- Power (+5V and +12V) and ground provided on DB78 board connector
- Includes 72" cable with DB78 Male connector to DB37 Male and DB37 Female connectors (Item# CA165)
- 8004Hec offers socketed dip resistors which allow for a user configurable input range up to +30V
- Software support for Linux (coming soon) and Windows



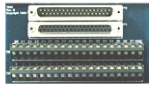
Optional Items

Depending upon your application, you are likely to find one or more of the following items useful with the 8004ec. All items can be purchased from our website (www.sealevel.com) by calling our sales team at (864) 843-4343.

Cables

DB37 Male to DB37 Female Extension Cable (Item# CA112)	
This cable provides a 6' extension to the CA165. It has one DB37 Male connector and one DB37 Female connector.	
DB78 Male to DB78 Female Extension Cable (Item# CA233)	
This cable provides a 6' extension to the DB78 board connector on the 8004ec. It has one DB78 Male connector and one DB78 Female connector.	
DB78M to DB37 Female and DB37 Male, for 3093 (Item# CA378)	
The CA378 is designed specifically for customers using the 3093 ISA digital I/O board that need to be upgraded to the 8004ec PCIe board while preserving existing infrastructure wiring. The CA378 connects to the 8004ec and provides an identical pin out to the 3093.	

Terminal Blocks

DB37 Terminal Block (Item# TB02)	
Break out serial and digital connectors to screw terminals for easy field connection. The TB02 terminal block is designed with both DB37 male and female connectors for interfacing the inputs or outputs.	

Other Sealevel PCI Digital I/O Products

Product	Item#	Description
PIO-32.PCI	8010	32 TTL Inputs/Outputs
PIO-48.PCI	8005	48 TTL Inputs/Outputs
PIO-96.PCI	8009	96 TTL Inputs/Outputs
REL-16.PCI	8003	16 Reed Relay Outputs
DIO-32.PCI	8004	16 Reed Relay Outputs/16 Opto-isolated Inputs
DIO-32.PCI	8004H	16 Reed Relay Outputs/16 Opto-isolated Inputs (10-30V)
ISO-16.PCI	8006	16 Opto-isolated Inputs
ISO-16.PCI	8006H	16 Opto-isolated Inputs (10-30V)
REL-32.PCI	8007	32 Reed Relay Outputs
PLC-16.PCI	8011	8 Form C Relay Outputs/8 Opto-isolated Inputs
DIO-16.PCI	8012	8 Reed Relay Outputs/8 Opto-isolated Inputs (3-13V)
DIO-16.PCI	8012H	8 Reed Relay Outputs/8 Opto-isolated Inputs (10-30V)
DIO-16.PCIE	8012e	8 Reed Relay Outputs/8 Opto-isolated Inputs (3-30V)

Electrical Specifications

Features

- 2 banks of SPST relays with 8 relays on each bank
- 2 banks of optically isolated inputs with 8 inputs on each bank
- DB-37 Male connector for relay outputs
- DB-37 Female connector for optically isolated inputs
- Highly reliable 10W / 10VA reed relays
- Multiple adapters can reside in same computer

Input Ports

Turn On Current	250 μ A
Isolator Diode Drop	1.2 VDC
Resistor Power Max	10mW
Absolute Maximum Input Voltage (8004ec)	30 VDC
Absolute Maximum Input Voltage (8004Hec)	36 VDC
Recommended Input Range (8004ec)	3 – 30 VDC
Recommended Input Range (8004Hec)	3 – 36 VDC
Maximum Input Current	25 mA

Output Relays

Contact Max Power Rating	10W / 10VA
Contact Voltage Maximum	60 VDC (or peak VAC)
Contact Current Maximum	.5A AC/DC RMS
Contact Resistance, Initial	150 m Ω
Contact Resonant Frequency	3.5 KHz
Contact Operate Time (max)	0.5 mS

Contact Release Time (max)	0.2 mS
Contact Bounce Time (max)	0.5 mS
Coil Must Operate Voltage	3.75 VDC
Coil Must Release Voltage	1.00 VDC
Coil Resistance, ±10%	500 mΩ

Technical Specifications

Physical Dimensions

Board length	6.5 inches (16.5 cm)
Board height	4.2 inches (10.7 cm)

Environmental Specifications

Specification	Operating	Storage
Temperature Range	0° to 70° C (32° to 158° F)	-50° to 105° C (-58° to 221° F)
Humidity Range	10 to 90% R.H. Non-Condensing	10 to 90% R.H. Non-Condensing

Manufacturing

All Sealevel Systems Printed Circuit boards are built to UL 94V0 rating and are 100% electrically tested. These printed circuit boards are solder mask over bare copper or solder mask over tin nickel.

Software Installation

Windows Installation



Do not connect the hardware until the software has been fully installed.



Only users running Windows 7 or newer should utilize these instructions for accessing and installing the appropriate driver via Sealevel's website. If you are utilizing an operating system prior to Windows 7, please contact Sealevel by calling 864.843.4343 or emailing support@sealevel.com to receive access to the proper driver download and installation instructions.

1. Begin by locating, selecting, and installing the correct software from the [Sealevel software driver database](#).
2. Type in or select the part number (**#8004ec**) for the adapter from the listing.
3. Select "Download Now" for Seal/O for Windows.
4. The setup files will automatically detect the operating environment and install the proper components. Follow the information presented on the screens that follow.
5. A screen may appear with text similar to: "The publisher cannot be determined due to the problems below: Authenticode signature not found." Please click the 'Yes' button and proceed with the installation. This declaration simply means that the operating system is not aware of the driver being loaded. It will not cause any harm to your system.
6. During setup, the user may specify installation directories and other preferred configurations. This program also adds entries to the system registry that are necessary for specifying the operating parameters for each driver. An uninstall option is also included to remove all registry/INI file entries from the system.
7. The software is now installed, and you can proceed with the hardware installation.



To install Sealevel software, you must log in as an administrator or have administrator privileges in Windows. All Sealevel Systems software drivers have been fully tested by Sealevel. Clicking "OK" button to continue.

Physical Installation



The Sealevel DIO-32.PCie can be installed in any PCIe expansion slot (x1/x4/x16).

PCI Express x1



To prevent damage from improper handling, please reference [Appendix B – ESD Handling Instructions](#).

1. Turn off PC power. Disconnect the power cord.
2. Remove the PC case cover.
3. Locate an available PCIe slot and remove the blank metal slot cover.
4. Gently insert the PCIe adapter into the slot. Make sure that the adapter is seated properly.
5. Replace the screw you removed for the blank and use it to secure the adapter bracket into the slot. This is required to ensure FCC Part 15 compliance.
6. Replace the cover.
7. Connect the power cord.

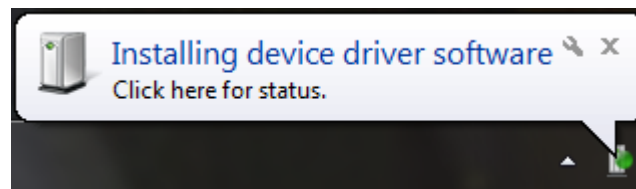
Hardware Installation



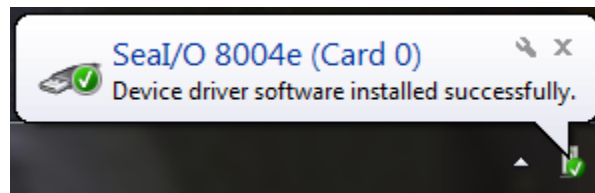
The DIO-32.PCIE does not need to be configured prior to installation.

Once you have installed the SeaI/O Classic software, install the board into an available PCI Express slot and boot the computer. The Found New Hardware wizard will appear. The drivers that were installed during the software installation process will automatically be used to configure the adapter.

1. After the software installation is complete, install the ISO-16.PCIE into an available PCI Express slot and boot the computer.
2. A 'Found New Hardware' alert will appear above the system tray.



3. When the 'Found New Hardware' alert informs you that your hardware is installed and ready to use, you can proceed with verifying the installation to check functionality if necessary.

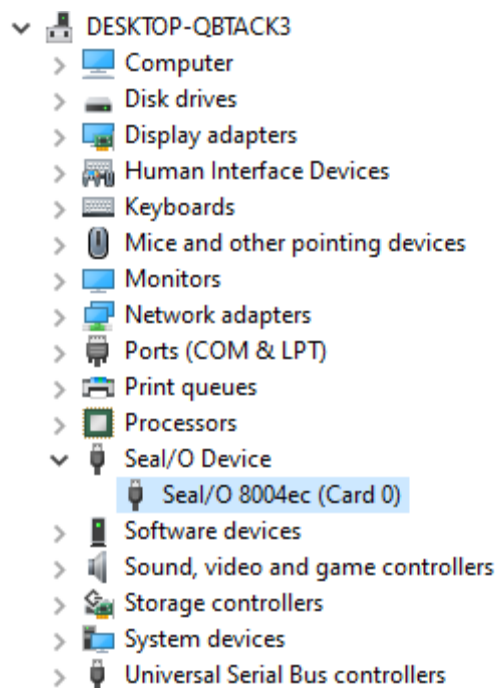


Verifying Hardware & Software Installation

You can use the Windows Device Manager to confirm that the digital I/O card has been successfully installed and recognized by your operating system, look in the Windows Device Manager.

To access Device Manager:

1. Right click on the Windows start button.
2. Click 'Device Manager' in the fly out menu.
3. In the right pane near the bottom, expand the 'Seal/O Device' section by clicking the '+' symbol. This shows the parent device is installed correctly.
4. You should see the card assignment listed as 'Seal/O 8004ec' with the card number in parentheses. The card number will increment for each additional Seal/O device installed.



The DIO-32.PCIe is now ready for use.

Programming the DIO-32.PCLe

Sealevel's Seal/O Classic software is provided to assist in the development of reliable applications for the Sealevel Systems family of PCI and PCI Express digital I/O adapters. Included in Sealevel's software are driver functions for use in accessing the I/O as well as helpful samples and utilities.

Programming for Windows

The Seal/O API (Application Programmer Interface) provides a variety of useful high-level function calls implemented in a Windows dynamic link library (DLL). The API is defined in the help file (Start/Programs/SealO/SealO Help) under "Application Programmers Interface." This help file also includes detailed information dealing with installation / removal of the software and information about latency, logic states, and device configuration.

For C language programmers we recommend using the API to access the SeaLINK DIO-32.PCI. If you are programming in Visual Basic, using the ActiveX control included with Seal/O is advised.

Programming for Linux

Seal/O for Linux consists of two major parts: a kernel module and a library. The kernel module is a simple IO pass-through device, allowing the library to handle the more sophisticated functions provided to Seal/O users. It is provided in a 'tarball' format and can easily be compiled and included in the kernel build.

Samples and Utilities

A variety of sample programs and utilities (both executable and source code) are included with Seal/O. On Windows, the samples can be found by navigating to "Start->Programs->SealO->Samples." On Linux, the samples are contained in the 'examples' folder within the downloaded tarball.

Register Layout

Reference the following sections for more information about the meaning and use of the registers available.

Address	Mode	Mode	D7	D6	D5	D4	D3	D2	D1	D0
Base+0	Input Port A	RD	PAD7	PAD6	PAD5	PAD4	PAD3	PAD2	PAD1	PAD0
Base+1	Input Port B	RD	PBD7	PBD6	PBD5	PBD4	PBD3	PBD2	PBD1	PBD0
Base+2	Output Port C	RD/WR	PCD7	PCD6	PCD5	PCD4	PCD3	PCD2	PCD1	PCD0
Base+3	Output Port D	RD	PDD7	PDD6	PDD5	PDD4	PDD3	PDD2	PDD1	PDD0
Base+A	Output Port D	RD/WR	PDD7	PDD6	PDD5	PDD4	PDD3	PDD2	PDD1	PDD0

Input Ports

Ports A and B are 8-bit input ports connected to optically isolated input sensors. Each sensor can be used to interface a voltage input and then sense whether the voltage is on or off. Each sensor is isolated (with respect to a common ground) from every other sensor, and isolated with respect to the host PC ground.

Each sensor input pair has a current limiting resistor that is used to limit the input current to the opto-isolator. The opto-isolator has two 'back-to-back' diodes internally. This DC signals to be sensed, regardless of polarity. When the applied voltage is high enough to cause the LED in the opto-isolator to turn-on, the output of the opto-isolator goes low (0 volts), and the signal is read as a low logic level (binary 0) by the PC. When the input signal is too low to turn on the opto-isolator, the output goes high, and the port bit is read by the PC as a high logic level (binary 1). While this is true at the hardware level, the Microsoft Windows driver gives the application the ability to use Positive or Negative Logic. This card defaults to Positive Logic and therefore the driver inverts the logic level BEFORE the information is returned to the application. Therefore, a voltage high enough to cause the LED to turn on will be read by the application as a logic high level.

The input impedance of each isolated input is approximately 8200 ohms (factory default). The opto-isolator requires approximately 250uA to turn on. The maximum input current is 25mA. The selection of the input resistor will determine the turn-on voltage for the inputs according to the 8004Hec Socketed Input Resistor Selection Table below. The voltage on the inputs must not exceed 30v for the 8004ec and 36v for the 8004Hec.

The following formulas apply:

$$\text{Turn on Voltage} = \text{diode drop} + (\text{turn on current}) \times (\text{resistance}) \text{ [Ex: } 1.2 + (.00025) \times R \text{]}$$

$$\text{Input Current} = ((\text{input voltage}) - 1.2\text{V}) / (\text{resistor value})$$



Increasing the input resistor accordingly can increase the turn-on voltage. Because socketed DIP resistors are utilized, they can easily be replaced with a different value. Sealevel can do this, if necessary.



The input circuits are not intended for monitoring 120-volt AC circuits. In addition to being too high a voltage for the circuits, it is dangerous to have that high a voltage on the card.

8004Hec Socketed Input Resistor Selection Table

The following table shows common input resistors and the ranges associated with each.

Input Resistor	Turn-On	Input Range	Max Input	Max Current	Notes
1200Ω	1.4V	1.4 – 18.0V	18.7V	15.8mA	
3300Ω	1.8V	1.8 – 29.0V	30.1V	9.2mA	
7600Ω	2.3V	2.3 – 34.0V	36.0V	6.1mA	
8200Ω	3.0V	3.0 – 34.0V	36.0V	5.8mA	Default Configuration
22.3KΩ	4.3V	4.3 – 34.0V	36.0V	3.5mA	
55.8KΩ	8.8V	8.8 – 34.0V	36.0V	2.2mA	
100KΩ	14.8V	14.8 – 34.0V	36.0V	1.6mA	
191KΩ	28.0V	28.0 – 34.0V	36.0V	1.2mA	



The 8004ec has surface mount input resistors which cannot be changed.

Input Ports Pin Assignments (DB-37 Female)

Inputs are interfaced via the DB-37 female connector on the supplied CA165 cable.

Port A Bit	Port A Input Pin Pairs	Port B Bit	Port B Input Pin Pairs
0	18,37	0	10,29
1	17,36	1	9, 28
2	16,35	2	8,27
3	15,34	3	7,26
4	14,33	4	6,25
5	13,32	5	5,24
6	12,31	6	4,23
7	11,30	7	3,22
Ground	2,20,21		
+12 Volts	1		
+5 Volts	19		



The CA165 cable input pin out is not compatible with the 3093 ISA digital I/O board. If you are upgrading from the 3093 and wish to preserve existing infrastructure wiring, order the CA378 cable.

Output Ports (Reed Relay)

Reed relays provide very high quality, long life, low current (10 Watt maximum), dry contact switch closures. Reed relays are not suited for high current applications, and can be destroyed by inductive load switching, where a spark occurs across the contacts internally. The relays are normally open, and close when energized.

Output Ports (Reed Relay) Pin Assignments (DB-37 Male)

Outputs are interfaced via the DB-37 male connector on the supplied CA165 cable.

Port C Bit	Relay	Port C Output Pin Pairs	Port D Bit	Relay	Port D Output Pin Pairs
0	K16	2,20	0	K8	10,28
1	K15	3,21	1	K7	11,29
2	K14	4,22	2	K6	12,30
3	K13	5,23	3	K5	13,31
4	K12	6,24	4	K4	14,32
5	K11	7,25	5	K3	15,33
6	K10	8,26	6	K2	16,34
7	K9	9,27	7	K1	17,35
Ground	18,36,37				
+ 5 Volts	19				
+ 12 Volts	1				

DB-78 Female Pin Assignments (Card Edge Connector)

Bit	Port A Pins	Port B Pins	Port C Pins	Port D Pins
0	55,74	47,66	2,20	10,28
1	54,73	46,65	3,21	11,29
2	53,72	45,64	4,22	12,30
3	52,71	44,63	5,23	13,31
4	51,70	43,62	6,24	14,32
5	50,69	42,61	7,25	15,33
6	49,68	41,60	8,26	16,34
7	48,67	40,59	9,27	17,35
GND	39,57,58		18,36,37	
+12V	38		1	
+5V	56		19	

Direct Hardware Control

In systems where the users program has direct access to the hardware (DOS) the tables that follow give the mapping and functions that the DIO-32.PCIe provides.

Function Available	Port	Address Hex	Port Type
RD	A	Base + 0	Optically Isolated Input Port
RD	B	Base + 1	
RD/WR	C	Base + 2	Reed Relay Output Port
RD	D	Base + 3	
RD/WR	D	Base + A	

RD = Read, RD/WR = Read or Write



To write to output bank D, use the Base + A register. The Base + 3 register is read only.

Reading the Inputs

The inputs are active Low. If no voltage is applied across one of the differential inputs, it returns a one on that bit. If a DC voltage is applied, it returns a zero on that bit.

Reading the Outputs

The relay ports return the ones complement of the value that is currently being used to drive the relays.

Writing the Outputs

The output ports are the only ports that can be written. The relays on a standard DIO-32.PCIe are normally open. To close a relay one must be written to the appropriate bit.

Register Description

All ports are set to input after reset or power up.

Address		Mode	D7	D6	D5	D4	D3	D2	D1	D0
Base+0	Input Port A	RD	PAD7	PAD6	PAD5	PAD4	PAD3	PAD2	PAD1	PAD0
Base+1	Input Port B	RD	PBD7	PBD6	PBD5	PBD4	PBD3	PBD2	PBD1	PBD0
Base+2	Output Port C	RD/WR	PCD7	PCD6	PCD5	PCD4	PCD3	PCD2	PCD1	PCD0
Base+3	Output Port D	RD	PDD7	PDD6	PDD5	PDD4	PDD3	PDD2	PDD1	PDD0
Base+A	Output Port D	RD/WR	PDD7	PDD6	PDD5	PDD4	PDD3	PDD2	PDD1	PDD0



To write to output bank D, use the Base + A register. The Base + 3 register is read only.

Optional 3093 Migration Cable (CA378)

On the 3093 ISA digital I/O board, the input pins #1 and #19 are reversed from the standard CA165 cable that ships standard with the 8004. If you are upgrading from a 3093 ISA board to an 8004 PCI board and wish to preserve existing infrastructure wiring, order the CA378 cable and use it in place of the standard CA165 cable.

CA378 Input Pin Assignments (DB-37 Female)

Inputs are interfaced via the DB-37 female connector on the optional CA378 cable.

Port A Bit	Port A Pins	Port B Bit	Port B Pins
0	18,37	0	10,29
1	17,36	1	9, 28
2	16,35	2	8,27
3	15,34	3	7,26
4	14,33	4	6,25
5	13,32	5	5,24
6	12,31	6	4,23
7	11,30	7	3,22

Ground	2,20,21		
+12 Volts	19		
+5 Volts	1		



The CA378 cable is designed specifically for customers upgrading from a 3093 ISA board to the 8004 PCI board. If you are not upgrading from the 3093 board, use the standard CA165 cable that ships with the 8004 PCI digital I/O board.

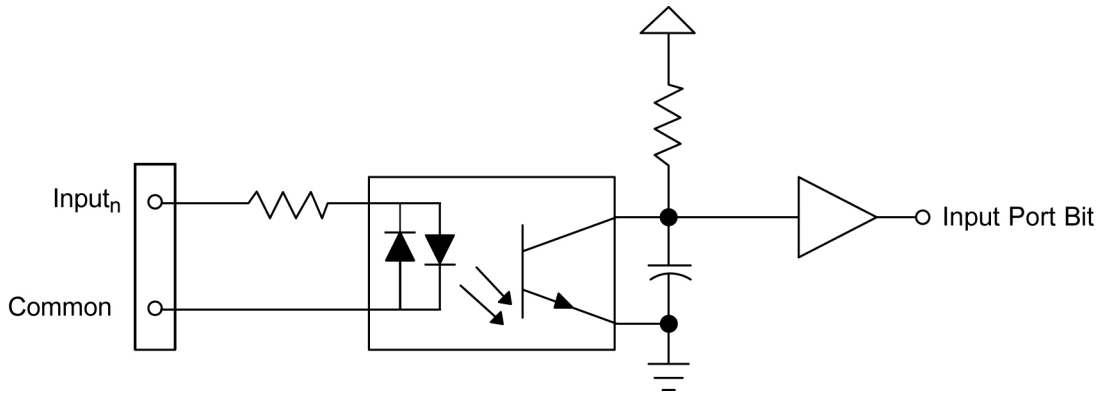
CA378 Output Pin Assignments (DB-37 Male)

Outputs are interfaced via the DB-37 male connector on the optional CA378 cable.

Port C Bit	Relay	Port C Pins	Port D Bit	Relay	Port D Pins
0	K16	2,20	0	K8	10,28
1	K15	3,21	1	K7	11,29
2	K14	4,22	2	K6	12,30
3	K13	5,23	3	K5	13,31
4	K12	6,24	4	K4	14,32
5	K11	7,25	5	K3	15,33
6	K10	8,26	6	K2	16,34
7	K9	9,27	7	K1	17,35
Ground	18,36,37				
+ 5 Volts	19				
+ 12 Volts	1				

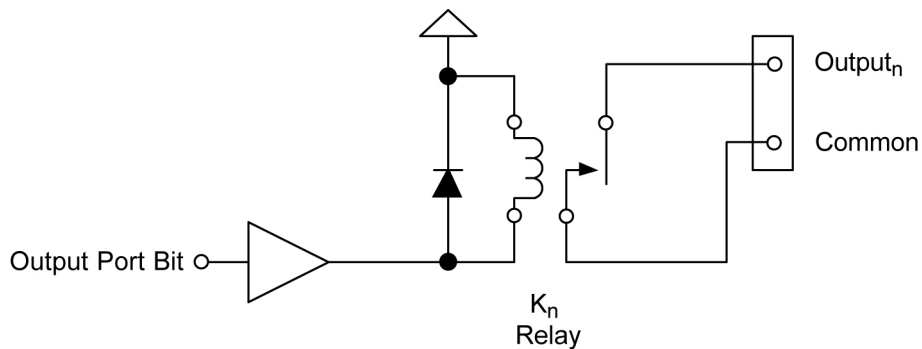
Example Circuits

INPUT CIRCUIT



In the above circuit diagram, $Input_n$ is one of the two Input Pair Pins from the table labeled Input Ports Pin Assignments. Common is the other pin listed in the Input Pair Pins entry. The polarity of the pair does not matter.

OUTPUT CIRCUIT



In the above circuit diagram, $Output_n$ is one of the two Output Pair Pins from the table labeled Output Ports Pin Assignments. Common is the other pin listed in Output Pair Pins. Since this is an isolated output, the polarity is not important.

Appendix A – Handling Instructions

ESD Warnings

Electrostatic Discharges (ESD)

A sudden electrostatic discharge can destroy sensitive components. Proper packaging and grounding rules must therefore be observed. Always take the following precautions:

1. Transport boards and cards in electrostatically secure containers or bags.
2. Keep electrostatically sensitive components in their containers until they arrive at an electrostatically protected workplace.
3. Only touch electrostatically sensitive components when you are properly grounded.
4. Store electrostatically sensitive components in protective packaging or on anti-static mats.

Grounding Methods

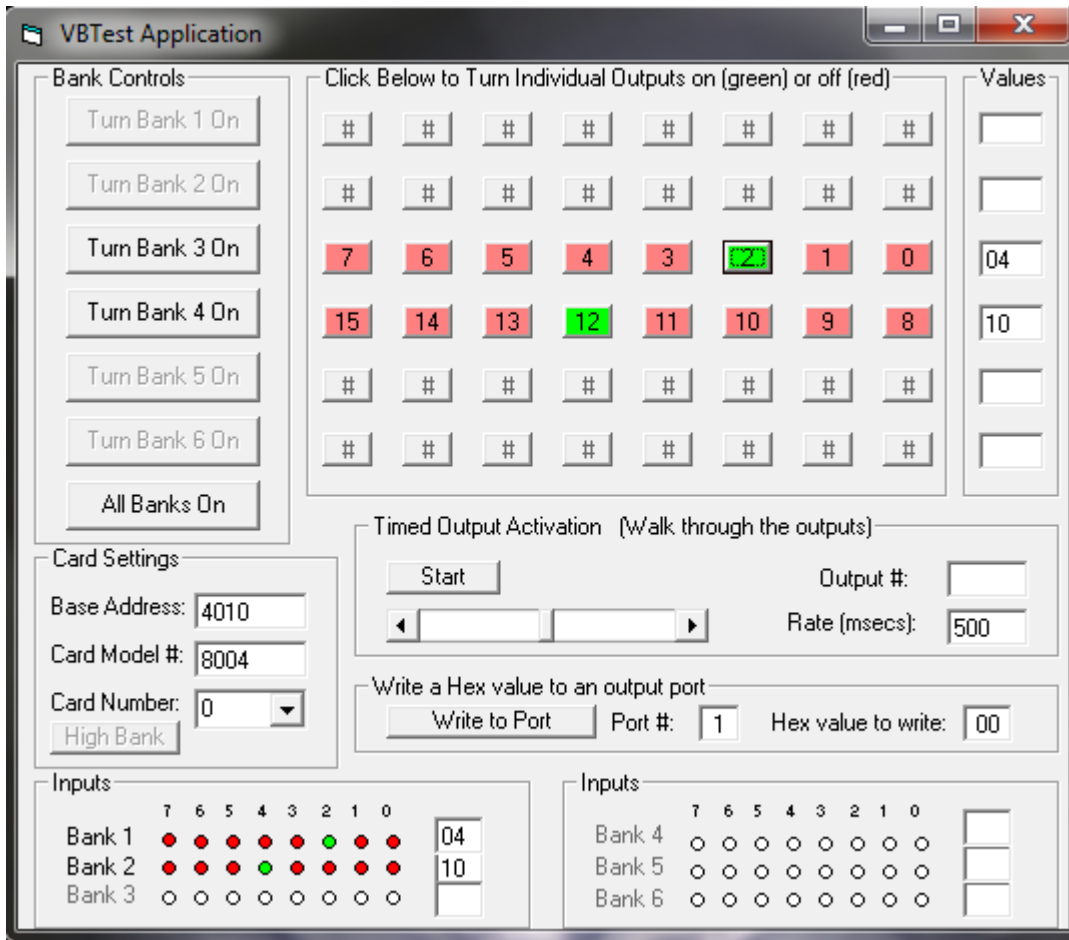
The following measures help to avoid electrostatic damages to the device:

1. Cover workstations with approved antistatic material. Always wear a wrist strap connected to a properly grounded workplace.
2. Use antistatic mats, heel straps, and/or air ionizers for more protection.
3. Always handle electrostatically sensitive components by their edge or by their casing.
4. Avoid contact with pins, leads, or circuitry.
5. Turn off power and input signals before inserting and removing connectors or connecting test equipment.
6. Keep the work area free of non-conductive materials such as ordinary plastic assembly aids and Styrofoam.
7. Use field service tools such as cutters, screwdrivers, and vacuum cleaners that are conductive.

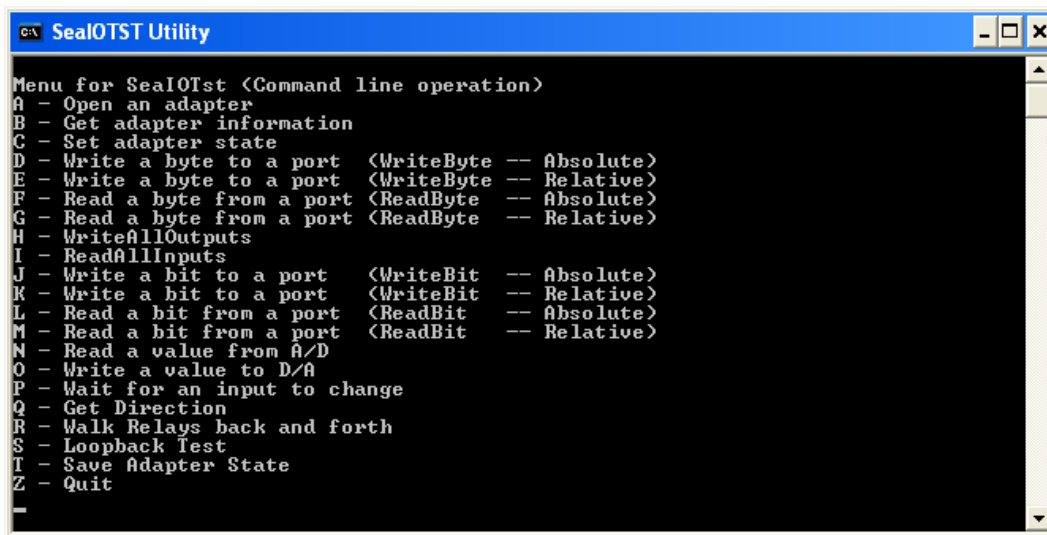
Appendix B – Troubleshooting

Following these simple steps can eliminate the most common problems.

1. Install software first. After installing the software then proceed to adding the hardware. This places the required installation files in the correct locations.
2. Read this manual thoroughly before attempting to install the adapter in your system.
3. Use Device Manager under Windows to verify proper installation. Refer to the Verifying Hardware section of this manual for instructions.
4. Several utilities with source code are included to verify the functionality of the inputs and outputs and to aid in application development. To test the inputs, you need to supply the turn-on voltage with a minimum of 3mA (maximum input current is 50mA) to trigger an input. Consult the pin out diagrams to test at the card edge connector or at the end of the cable.
5. The VBTest utility is a GUI tool included with Seal/O Classic software to easily access the I/O of installed Seal/O Classic devices. The source code is included to aid with Visual Basic application development.



- You may also use SealOTST, which is a command line utility that allows you to test the function calls from the Seal/O Classic API.



```
SealOTST Utility
Menu for SeaIOTst <Command line operation>
A - Open an adapter
B - Get adapter information
C - Set adapter state
D - Write a byte to a port <WriteByte -- Absolute>
E - Write a byte to a port <WriteByte -- Relative>
F - Read a byte from a port <ReadByte -- Absolute>
G - Read a byte from a port <ReadByte -- Relative>
H - WriteAllOutputs
I - ReadAllInputs
J - Write a bit to a port <WriteBit -- Absolute>
K - Write a bit to a port <WriteBit -- Relative>
L - Read a bit from a port <ReadBit -- Absolute>
M - Read a bit from a port <ReadBit -- Relative>
N - Read a value from A/D
O - Write a value to D/A
P - Wait for an input to change
Q - Get Direction
R - Walk Relays back and forth
S - Loopback Test
T - Save Adapter State
Z - Quit
_
```

N The source code for all utilities is located in C:\Program Files\SealO\Samples

N The API is documented in the SealO help file: Start -> Sealevel Systems - SealO -> SealO Help.

If these steps do not solve your problem, please call Sealevel Systems' Technical Support, (864) 843-4343. Our technical support is free and available from 8:00 A.M.- 5:00 P.M. Eastern Time Monday through Friday. For email support contact support@sealevel.com.

Appendix C – How To Get Assistance

Begin by reading through the Troubleshooting Guide in [Appendix B](#). If assistance is still needed, please see below.

When calling for technical assistance, please have your user manual and current adapter settings. If possible, please have the adapter installed in the computer ready to run diagnostics.

Sealevel Systems provides an FAQ section on its web site. Please refer to this to answer many common questions. This section can be found at <http://www.sealevel.com/faq.asp>.

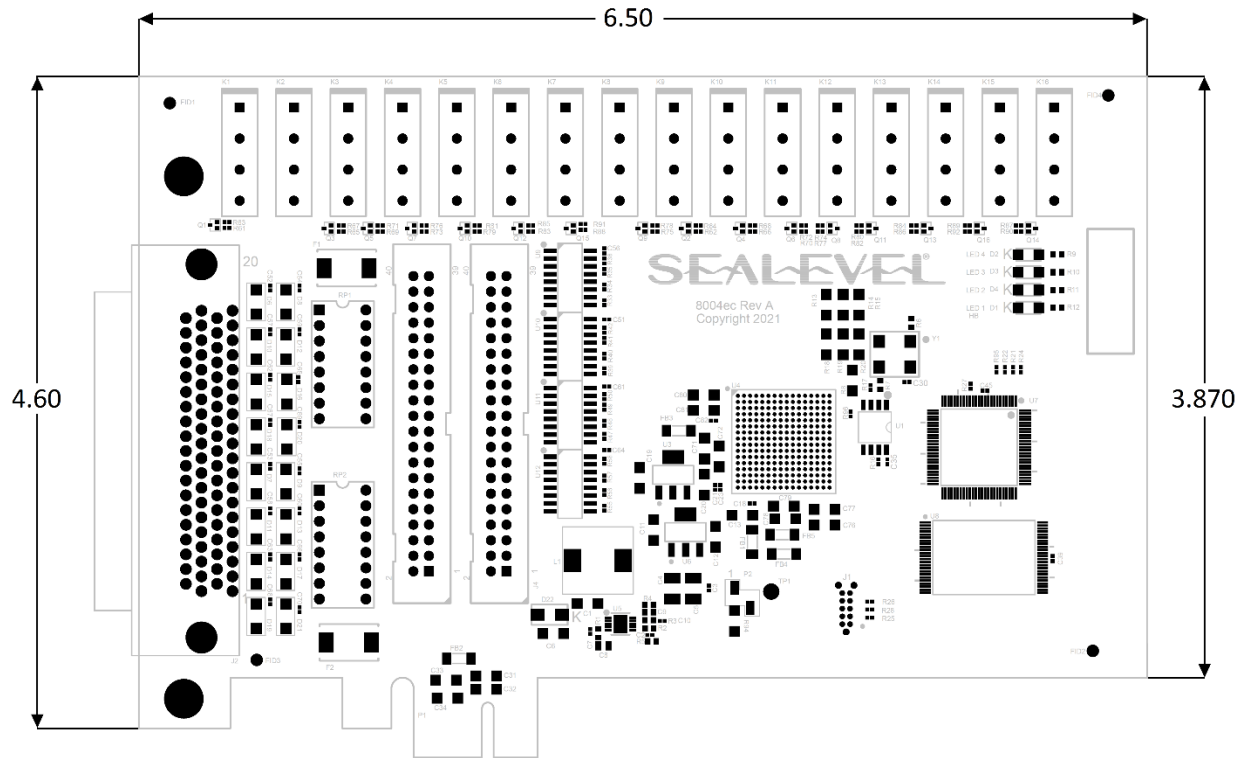
Sealevel Systems maintains a Home page on the Internet. Our home page address is <http://www.sealevel.com>. The latest software updates, and newest manuals are available via our FTP site that can be accessed from our home page.

Technical support is available Monday to Friday from 8:00 A.M. to 5:00 P.M. Eastern Time. Technical support can be reached at (864) 843-4343.

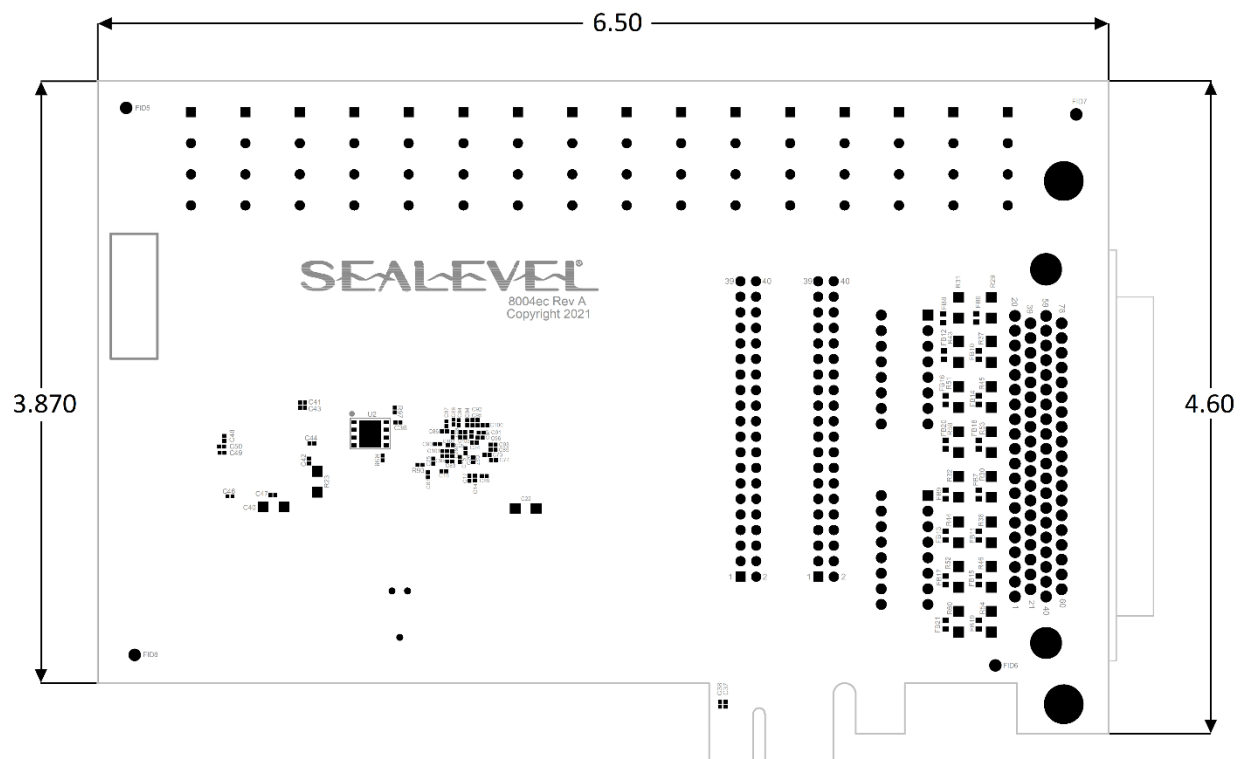
RETURN AUTHORIZATION MUST BE OBTAINED FROM SEALEVEL SYSTEMS BEFORE RETURNED MERCHANDISE WILL BE ACCEPTED. AUTHORIZATION CAN BE OBTAINED BY CALLING SEALEVEL SYSTEMS AND REQUESTING A RETURN MERCHANDISE AUTHORIZATION (RMA) NUMBER.

Appendix D – Drawings

Top



Bottom



Appendix E – Compliance Notices

Federal Communications Commission (FCC) Statement



This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

ISED Canada

- **CAN ICES-003(A) / NMB-003(A)**

EMC Directive Statement



This equipment has been evaluated or tested and found in compliance with the requirements of the following directives issued by the European Commission:

- EMC Directive **2014/30/EU**
- RoHS Directive **2011/65/EU + (EU) 2015/863**



CE marking is recognized in the UK as an acceptable method of demonstrating compliance for certain categories of products, including one described in this manual.



This is a Class A Product. In a domestic environment, this product may cause radio interference in which case the user may be required to take adequate measures to prevent or correct the interference.



Always use the cabling provided with this product if possible. If no cable is provided or if an alternate cable is required, use high quality shielded cabling to maintain compliance with FCC/EMC directives.

Warranty

Sealevel's commitment to providing the best I/O solutions is reflected in the Lifetime Warranty that is standard on all Sealevel manufactured I/O products. We are able to offer this warranty due to our control of manufacturing quality and the historically high reliability of our products in the field. Sealevel products are designed and manufactured at its Liberty, South Carolina facility, allowing direct control over product development, production, burn-in and testing. Sealevel achieved ISO-9001:2015 certification in 2018.

Warranty Policy

Sealevel Systems, Inc. (hereafter "Sealevel") warrants that the Product shall conform to and perform in accordance with published technical specifications and shall be free of defects in materials and workmanship for the warranty period. In the event of failure, Sealevel will repair or replace the product at Sealevel's sole discretion. Failures resulting from misapplication or misuse of the Product, failure to adhere to any specifications or instructions, or failure resulting from neglect, abuse, accidents, or acts of nature are not covered under this warranty.

Warranty service may be obtained by delivering the Product to Sealevel and providing proof of purchase. Customer agrees to ensure the Product or assume the risk of loss or damage in transit, to prepay shipping charges to Sealevel, and to use the original shipping container or equivalent. The warranty is valid only for the original purchaser and is not transferable.

This warranty applies to Sealevel manufactured Product. Product purchased through Sealevel but manufactured by a third party will retain the original manufacturer's warranty.

Non-Warranty Repair/Retest

Products returned due to damage or misuse and Products retested with no problem found are subject to repair/retest charges. A purchase order or credit card number and authorization must be provided in order to obtain an RMA (Return Merchandise Authorization) number prior to returning the Product.

How to obtain an RMA (Return Merchandise Authorization)

If you need to return a product for warranty or non-warranty repair, you must first obtain an RMA number. Please contact Sealevel Systems, Inc. Technical Support for assistance:

Available	Monday – Friday, 8:00 AM to 5:00 PM EST
Phone	864-843-4343
Email	support@sealevel.com

Trademarks

Sealevel Systems, Incorporated acknowledges that all trademarks referenced in this manual are the service mark, trademark, or registered trademark of the respective company.