DIO- 32.PCI User Manual

Part Number 8004H
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Introduction

The DIO-32.PCI digital I/O interface provides 16 optically isolated inputs and 16 reed relay outputs. The inputs (rated for 10-30V) protect the PC and other sensitive equipment from spikes and ground loop current that can be generated in industrial environments, while the outputs provide high quality, long life, low current (10 Watt maximum), dry contact switch closures. Reed relays are well suited for low current applications. The relays are normally open, and close when energized.

The DIO-32.PCI is designed to be used with a variety of Operating Systems including Windows 98/NT/ME/2000/XP, Linux and DOS. The Seal/O API (Application Programmer Interface) included on CD with the DIO-32.PCI provides a variety of useful high-level function calls implemented as a Windows dynamic link library (DLL) and as a Linux kernel module and library. In addition to the API, Seal/O includes sample code and utilities to simplify software development.

Other Sealevel PCI Digital I/O Products

<table>
<thead>
<tr>
<th>Product</th>
<th>Part Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>PIO-24.PCI</td>
<td>P/N 8008</td>
<td>24 TTL Inputs/Outputs</td>
</tr>
<tr>
<td>PIO-32.PCI</td>
<td>P/N 8010</td>
<td>32 TTL Inputs/Outputs</td>
</tr>
<tr>
<td>PIO-48.PCI</td>
<td>P/N 8005</td>
<td>48 TTL Inputs/Outputs</td>
</tr>
<tr>
<td>PIO-96.PCI</td>
<td>P/N 8009</td>
<td>96 TTL Inputs/Outputs</td>
</tr>
<tr>
<td>DIO-16.PCI</td>
<td>P/N 8002</td>
<td>8 Reed Relay Outputs/8 Opto-isolated Inputs</td>
</tr>
<tr>
<td>REL-16.PCI</td>
<td>P/N 8003</td>
<td>16 Reed Relay Outputs</td>
</tr>
<tr>
<td>ISO-16.PCI</td>
<td>P/N 8006</td>
<td>16 Opto-isolated Inputs</td>
</tr>
<tr>
<td>REL-32.PCI</td>
<td>P/N 8007</td>
<td>32 Reed Relay Outputs</td>
</tr>
<tr>
<td>PLC-16.PCI</td>
<td>P/N 8011</td>
<td>8 Form C Relay Outputs/8 Opto-isolated Inputs</td>
</tr>
</tbody>
</table>
Before You Get Started

What's Included

The DIO-32.PCI is shipped with the following items. If any of these items is missing or damaged please contact Sealevel for replacement.

- Item# 8004H – DIO-32.PCI Adapter
- Item# CA165 – DB-78 Male to DB-37 Male and DB-37 Female V-cable
- Sealevel SeaI/O Software CD

Optional Items

Depending upon your application, you are likely to find one or more of the following items useful for interfacing the DIO-32.PCI to real-world signals. All items can be purchased from our website (http://www.sealevel.com) or by calling (864) 843-4343.

- DB-37 Male to DB-37 Female Extension Cable - (Item# CA112)
  - This cable provides a 6’ extension to the CA165. It has one DB37 Male connector and one DB37 Female connector.

- DB-37 Male/Female Terminal Block (Item# TB02)
  - Break out serial and digital connectors to screw terminals for easy field connection. The TB02 terminal block is designed with both DB37 male and female connectors, therefore; it can be used with any DB37 board regardless of the board's port gender.

- Cable and Terminal Block Kit (Item# KT101)
  - The KT101 includes the TB02 terminal block and CA112 cable. To fully connect the DIO-32.PCI two KT101 kits will be required.
Software Installation

Windows 98/ ME/ NT/ 2000/ XP Installation

1. Start Windows.
2. Insert the Sealevel Systems CD in to your CD drive.
3. If ‘Auto-Start’ is enabled for this drive the software will automatically launch. Otherwise, point your browser to the ‘Index.htm’ on the root directory of the CD.
4. Select ‘Install Software’.
5. Select the Part Number for your adapter from the listing.
6. Select ‘Windows 98/ME/NT/2000/XP’. The setup file will automatically detect the operating environment and install the proper components. Next (depending on your browser) select the ‘Run this program from its current location’ or ‘Open’ option. Follow the information presented on the screens that follow.
7. A screen may appear with the declaration: “The publisher cannot be determined due to the problems below: Authenticode signature not found.” Please select the ‘Yes’ button and proceed with the installation. This declaration simply means that the Operating System is not aware of the driver being loaded. It will not cause any harm to your system.
8. During setup the user may specify installation directories and other preferred configurations. This program also adds entries to the system registry that are necessary for specifying the operating parameters for each driver. An uninstall option is also included to remove all registry/INI file entries from the system.

Windows NT Card Installation: After accomplishing the above steps, bring up the Control Panel and double-click on the SeaIO Devices icon. To install a new card, click "Add Port". Repeat this procedure for as many SeaIO cards as you wish to install.
**Linux Installation**

**Note:** You MUST have "root" privileges to install the software and drivers.

1. Login as "root".
2. Mount the CDROM by typing:
   ```
   mount -t iso9660 /dev/hdc /cdrom
   ```

**Note** Your cdrom may not be /dev/hdc it could be /dev/hda, /dev/hdb, /dev/hdd, or if you have a SCSI drive /dev/sda, /dev/sdb, /dev/sdc, etc. You may mount the CDROM to any location, the /cdrom is just a common example.

3. Next change to the directory where you mounted the CDROM:
   ```
   Ex. cd /cdrom/software/SeaIO/Other/linux
   ```

**Note:** The syntax is case sensitive.

4. Copy seaio .tar.gz to your home directory by typing:
   ```
   cp seaio .tar.gz ~
   ```

5. Change to your home directory by typing:
   ```
   cd
   ```

6. Unmount the drive and then Unzip and Untar the drivers and software by typing:
   ```
   umount /cdrom
   tar -xvzf seaio .tar.gz
   ```

7. Change to the SeaIO directory by typing:
   ```
   cd SeaIO
   ```

8. Now compile and prepare the drivers for use:
   ```
   make install
   ```

9. With the system off and unplugged, install your SeaIO PCI card.

10. Plug system back in and boot Linux.
    ```
    Login as "root".
    ```

11. Load the driver by typing:
    ```
    SeaIO -load
    ```

The driver has enabled the card and is ready to use, and you now have the option to run a test utility on it. Skip to section "Using the test software" if you wish to do so at this time.

To set up Linux to automatically load the driver; refer to a Linux manual concerning your specific distribution for help.
Physical Installation

The adapter can be installed in any 5V PCI expansion slot.

Do not install the Adapter in the machine until the software has been fully installed.

1. Turn off PC power. Disconnect the power cord.
2. Remove the PC case cover.
3. Locate an available 5V PCI slot and remove the blank metal slot cover.
4. Gently insert the PCI adapter into the slot. Make sure that the adapter is seated properly.
5. After the adapter has been installed, the cables should be routed thru the opening in the bracket. This bracket also features a strain relief function that should be used to prevent un-expected cable removal.
6. Replace the screw you removed for the blank and use it to secure the adapter into the slot. (This is required to ensure FCC Part 15 compliance.)
7. Replace the cover.
8. Connect the power cord

The DIO-32.PCI is now ready for use.
Sealevel’s SeaI/O software is provided to assist in the development of reliable applications for the Sealevel Systems family of digital I/O adapters. Included on the SeaI/O CD are driver functions for use in accessing the I/O as well as helpful samples and utilities.

**Programming for Windows**

The SeaI/O API (Application Programmer Interface) provides a variety of useful high-level function calls implemented in a Windows dynamic link library (DLL). The API is defined in the help file (Start/Programs/SeaIO/SeaIO Help) under “Application Programmers Interface”. This help file also includes detailed information dealing with installation / removal of the software and information about latency, logic states, and device configuration.

For C language programmers we recommend using the API to access the DIO-32.PCI. If you are programming in Visual Basic, using the ActiveX control included with Seal/O is advised.

**Samples and Utilities**

A variety of sample programs and utilities (both executable and source code) are included with Seal/O. Further documentation on these samples can be found by selecting “Start/Programs/SeaIO/Sample Application Description”. Information about where the files are physically stored on your disk is also included in this same file.

**Programming for Linux**

Seal/O for Linux consists of two major parts: a kernel module and a library. The kernel module is a simple IO pass-through device, allowing the library to handle the more sophisticated functions provided to SeaI/O users. It is provided in a ‘tarball’ format and can easily be compiled and included in the kernel build.

**Digital I/O Interface**

The DIO-32.PCI provides four parallel input/output (I/O) ports. The ports are organized as ports A, B, C, and D. Port A and B are input ports interfaced to optically-isolated inputs, while ports C and D are reed relay output ports. Assuming an I/O address of 300 Hex the following table shows the Port Addresses.

<table>
<thead>
<tr>
<th>Base Address</th>
<th>Hex</th>
<th>Decimal</th>
<th>Mode</th>
</tr>
</thead>
<tbody>
<tr>
<td>Port A Address</td>
<td>300</td>
<td>768</td>
<td>Optically Isolated Input Port</td>
</tr>
<tr>
<td>Port B Address</td>
<td>301</td>
<td>769</td>
<td></td>
</tr>
<tr>
<td>Port C Address</td>
<td>302</td>
<td>770</td>
<td>Reed Relay Output Port</td>
</tr>
<tr>
<td>Port D Address</td>
<td>303</td>
<td>771</td>
<td></td>
</tr>
</tbody>
</table>
Input Ports

Ports A and B are 8 bit input ports connected to optically isolated input sensors. Each sensor can be used to interface a voltage input and then sense whether the voltage is on or off. Each sensor is isolated (with respect to a common ground) from every other sensor, and also isolated with respect to the host PC ground. This means that signals such as low-level AC line voltage, motor servo voltage, and control relay signals can be ‘sensed’, or read by the PC, without the risk of damage due to ground loops or ground faults.

Each sensor input pair has a current limiting resistor that is used to limit the input current to the opto-isolator. The opto-isolator has two ‘back-to-back’ diodes internally. This allows AC or DC signals to be sensed, regardless of polarity. When the applied voltage is high enough to cause the led in the opto-isolator to turn-on, the output of the opto-isolator goes low (0 volts) and the signal is read as a low logic level (binary 0) by the PC. When the input signal is too low to turn on the opto-isolator, the output goes high and the port bit is read by the PC as a high logic level (binary 1).

The input impedance of each isolated input is approximately 3.3K ohms (factory default). The opto-isolator requires approximately 3mA to turn on. The maximum input current is 50mA. There are two things to consider when selecting the input resistor. The first is turn on voltage for the circuit to sense, and second is the maximum input voltage. Maximum input voltage must not provide too much power to the input resistor, and must also not overdrive the opto-isolator input current specification. The following formulas apply:

\[
\text{Turn on Voltage} = \text{diode drop} + (\text{turn on current}) \times (\text{resistance}) \quad [\text{Ex: } 1.1 + (0.003) \times R]
\]

\[
\text{Input Current} = ((\text{input voltage}) - 1.1V) / (\text{resistor value})
\]

\[
\text{Maximum voltage} = 1.1 + \sqrt{0.25(\text{resistor value})}
\]

The following table shows common input resistors and the ranges associated with each.

<table>
<thead>
<tr>
<th>Input Resistor</th>
<th>Turn-On</th>
<th>Input Range</th>
<th>Max Input</th>
<th>Max Current</th>
</tr>
</thead>
<tbody>
<tr>
<td>220Ω</td>
<td>1.8V</td>
<td>1.8 – 7.0V</td>
<td>8.5V</td>
<td>27mA</td>
</tr>
<tr>
<td>560Ω</td>
<td>2.8V</td>
<td>2.8 – 10.6V</td>
<td>12.9V</td>
<td>20mA</td>
</tr>
<tr>
<td>1KΩ</td>
<td>4.1V</td>
<td>4.1 – 13.8V</td>
<td>16.9V</td>
<td>15mA</td>
</tr>
<tr>
<td>2.2KΩ</td>
<td>7.7V</td>
<td>7.7 – 20.0V</td>
<td>24.5V</td>
<td>10mA</td>
</tr>
<tr>
<td>3.3KΩ</td>
<td>10.0V</td>
<td>10.0 – 24.0V</td>
<td>30.0V</td>
<td>9mA</td>
</tr>
<tr>
<td>4.7KΩ</td>
<td>15.2V</td>
<td>15.2 – 28.0V</td>
<td>35.0V</td>
<td>7mA</td>
</tr>
</tbody>
</table>

Note: The turn-off voltage for all resistors is less than 1V.

Increasing the input resistor accordingly can increase the maximum input voltage. Because socketed DIP resistors are utilized, they can easily be replaced with a different value. Sealevel, if necessary can do this.

Important: The input circuits are not intended for monitoring 120-volt AC circuits. In addition to being too high a voltage for the circuits, it is dangerous to have that high a voltage on the card.
Input Ports Pin Assignments (DB-37 Female)

Inputs are interfaced via the DB-37 female connector on the supplied CA165 cable.

<table>
<thead>
<tr>
<th>Port A Bit</th>
<th>Port A Pins</th>
<th>Port B Bit</th>
<th>Port B Pins</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>18,37</td>
<td>0</td>
<td>10,29</td>
</tr>
<tr>
<td>1</td>
<td>17,36</td>
<td>1</td>
<td>9,28</td>
</tr>
<tr>
<td>2</td>
<td>16,35</td>
<td>2</td>
<td>8,27</td>
</tr>
<tr>
<td>3</td>
<td>15,34</td>
<td>3</td>
<td>7,26</td>
</tr>
<tr>
<td>4</td>
<td>14,33</td>
<td>4</td>
<td>6,25</td>
</tr>
<tr>
<td>5</td>
<td>13,32</td>
<td>5</td>
<td>5,24</td>
</tr>
<tr>
<td>6</td>
<td>12,31</td>
<td>6</td>
<td>4,23</td>
</tr>
<tr>
<td>7</td>
<td>11,30</td>
<td>7</td>
<td>3,22</td>
</tr>
</tbody>
</table>

Ground 2,20,21
+12 Volts 1
+5 Volts 19

NOTE: The CA165 cable input pin out is not compatible with the 3093 ISA digital I/O board. If you are upgrading from the 3093 and wish to preserve existing infrastructure wiring, order the CA378 cable.

Output Ports (Reed Relay)

Reed relays provide very high quality, long life, low current (10 Watt maximum), dry contact switch closures. Reed relays are not suited for high current applications, and can be destroyed by inductive load switching, where a spark occurs across the contacts internally. The relays are normally open, and close when energized.

Output Ports (Reed Relay) Pin Assignments (DB-37 Male)

Outputs are interfaced via the DB-37 male connector on the supplied CA165 cable.

<table>
<thead>
<tr>
<th>Port C Bit</th>
<th>Relay</th>
<th>Port C Pins</th>
<th>Port D Bit</th>
<th>Relay</th>
<th>Port D Pins</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>K16</td>
<td>2,20</td>
<td>0</td>
<td>K8</td>
<td>10,28</td>
</tr>
<tr>
<td>1</td>
<td>K15</td>
<td>3,21</td>
<td>1</td>
<td>K7</td>
<td>11,29</td>
</tr>
<tr>
<td>2</td>
<td>K14</td>
<td>4,22</td>
<td>2</td>
<td>K6</td>
<td>12,30</td>
</tr>
<tr>
<td>3</td>
<td>K13</td>
<td>5,23</td>
<td>3</td>
<td>K5</td>
<td>13,31</td>
</tr>
<tr>
<td>4</td>
<td>K12</td>
<td>6,24</td>
<td>4</td>
<td>K4</td>
<td>14,32</td>
</tr>
<tr>
<td>5</td>
<td>K11</td>
<td>7,25</td>
<td>5</td>
<td>K3</td>
<td>15,33</td>
</tr>
<tr>
<td>6</td>
<td>K10</td>
<td>8,26</td>
<td>6</td>
<td>K2</td>
<td>16,34</td>
</tr>
<tr>
<td>7</td>
<td>K9</td>
<td>9,27</td>
<td>7</td>
<td>K1</td>
<td>17,35</td>
</tr>
</tbody>
</table>

Ground 18,36,37
+5 Volts 19
+12 Volts 1
**DB- 78 Female Pin Assignments (Card Edge Connector)**

<table>
<thead>
<tr>
<th>Bit</th>
<th>Port A Pins</th>
<th>Port B Pins</th>
<th>Port C Pins</th>
<th>Port D Pins</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>55,74</td>
<td>47,66</td>
<td>2,20</td>
<td>10,28</td>
</tr>
<tr>
<td>1</td>
<td>54,73</td>
<td>46,65</td>
<td>3,21</td>
<td>11,29</td>
</tr>
<tr>
<td>2</td>
<td>53,72</td>
<td>45,64</td>
<td>4,22</td>
<td>12,30</td>
</tr>
<tr>
<td>3</td>
<td>52,71</td>
<td>44,63</td>
<td>5,23</td>
<td>13,31</td>
</tr>
<tr>
<td>4</td>
<td>51,70</td>
<td>43,62</td>
<td>6,24</td>
<td>14,32</td>
</tr>
<tr>
<td>5</td>
<td>50,69</td>
<td>42,61</td>
<td>7,25</td>
<td>15,33</td>
</tr>
<tr>
<td>6</td>
<td>49,68</td>
<td>41,60</td>
<td>8,26</td>
<td>16,34</td>
</tr>
<tr>
<td>7</td>
<td>48,67</td>
<td>40,59</td>
<td>9,27</td>
<td>17,35</td>
</tr>
<tr>
<td>GND</td>
<td>39,57,58</td>
<td>18,36,37</td>
<td></td>
<td></td>
</tr>
<tr>
<td>+12V</td>
<td>38</td>
<td></td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>+5V</td>
<td>56</td>
<td></td>
<td></td>
<td>19</td>
</tr>
</tbody>
</table>

**Direct Hardware Control**

In systems where the users program has direct access to the hardware (DOS) the tables that follow give the mapping and functions that the DIO-32.PCI provides.

<table>
<thead>
<tr>
<th>Function Available</th>
<th>Port</th>
<th>Address Hex</th>
<th>Port Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>RD</td>
<td>A</td>
<td>Base + 0</td>
<td>Optically Isolated Input Port</td>
</tr>
<tr>
<td>RD</td>
<td>B</td>
<td>Base + 1</td>
<td>Reed Relay Output Port</td>
</tr>
<tr>
<td>RD/WR</td>
<td>C</td>
<td>Base + 2</td>
<td></td>
</tr>
<tr>
<td>RD/WR</td>
<td>D</td>
<td>Base + 3</td>
<td></td>
</tr>
</tbody>
</table>

*RD = Read, RD/WR = Read or Write*

**Reading the Inputs**

The inputs are active Low. If no voltage is applied across one of the differential inputs it returns a one on that bit. If an AC or DC voltage is applied it returns a zero on that bit.

**Reading the Outputs**

The relay ports return the ones complement of the value that is currently being used to drive the relays.

**Writing the Outputs**

The output ports are the only ports that can be written. The relays on a standard DIO-32.PCI are normally open. To close a relay a one must be written to the appropriate bit.
Register Description

All ports are set to input after reset or power up.

<table>
<thead>
<tr>
<th>Address</th>
<th>Mode</th>
<th>D7</th>
<th>D6</th>
<th>D5</th>
<th>D4</th>
<th>D3</th>
<th>D2</th>
<th>D1</th>
<th>D0</th>
</tr>
</thead>
<tbody>
<tr>
<td>Base+0</td>
<td>Input Port A</td>
<td>RD</td>
<td>PAD7</td>
<td>PAD6</td>
<td>PAD5</td>
<td>PAD4</td>
<td>PAD3</td>
<td>PAD2</td>
<td>PAD1</td>
</tr>
<tr>
<td>Base+1</td>
<td>Input Port B</td>
<td>RD</td>
<td>PBD7</td>
<td>PBD6</td>
<td>PBD5</td>
<td>PBD4</td>
<td>PBD3</td>
<td>PBD2</td>
<td>PBD1</td>
</tr>
<tr>
<td>Base+2</td>
<td>Output Port C</td>
<td>RD/WR</td>
<td>PCD7</td>
<td>PCD6</td>
<td>PCD5</td>
<td>PCD4</td>
<td>PCD3</td>
<td>PCD2</td>
<td>PCD1</td>
</tr>
<tr>
<td>Base+3</td>
<td>Output Port D</td>
<td>RD/WR</td>
<td>PDD7</td>
<td>PDD6</td>
<td>PDD5</td>
<td>PDD4</td>
<td>PDD3</td>
<td>PDD2</td>
<td>PDD1</td>
</tr>
<tr>
<td>Base+4</td>
<td></td>
<td>RD</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>Base+5</td>
<td>Interrupt Status</td>
<td>RD/WR</td>
<td>IRQEN</td>
<td>IRQST</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

Interrupt Control

When enabled, interrupts are generated on Port A bit D0.

<table>
<thead>
<tr>
<th>IRQEN</th>
<th>Interrupt enable</th>
<th>1 = enabled</th>
<th>0 = disabled ( 0 on power up )</th>
</tr>
</thead>
<tbody>
<tr>
<td>IRC0</td>
<td></td>
<td>Interrupt mode select, see table below</td>
<td></td>
</tr>
<tr>
<td>IRC1</td>
<td></td>
<td>Interrupt mode select, see table below</td>
<td></td>
</tr>
</tbody>
</table>

Interrupt Mode Select Table

Interrupt source is Base+0 bit D0. When selecting the Interrupt Type, always disable interrupts prior to changing or setting states. This will help prevent inadvertent or unexpected interrupts from occurring.

<table>
<thead>
<tr>
<th>IRC1</th>
<th>IRC0</th>
<th>Interrupt Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>Low Level</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>High Level</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>Falling Edge</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>Rising Edge</td>
</tr>
</tbody>
</table>

Warning: When using the High and Low Level interrupts, an interrupt occurs when input D0 changes to either a High or Low state. This will cause the computer to remain in an interrupt state until the input state changes.

Interrupt Read

Reading the Interrupt Status port (Base+5) clears any interrupt pending.

<table>
<thead>
<tr>
<th>IRQST</th>
<th>(D0) Interrupt Status</th>
<th>1 = interrupt pending, 0 = none</th>
</tr>
</thead>
</table>

Optional 3093 Migration Cable (CA378)

On the 3093 ISA digital I/O board, the input pins #1 and #19 are reversed from the standard CA165 cable that ships standard with the 8004. If you are upgrading from a 3093 ISA board to an 8004 PCI board and wish to preserve existing infrastructure wiring, order the CA378 cable and use it in place of the standard CA165 cable.

CA378 Input Pin Assignments (DB-37 Female)

Inputs are interfaced via the DB-37 female connector on the optional CA378 cable.

<table>
<thead>
<tr>
<th>Port A Bit</th>
<th>Port A Pins</th>
<th>Port B Bit</th>
<th>Port B Pins</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>18,37</td>
<td>0</td>
<td>10,29</td>
</tr>
<tr>
<td>1</td>
<td>17,36</td>
<td>1</td>
<td>9,28</td>
</tr>
<tr>
<td>2</td>
<td>16,35</td>
<td>2</td>
<td>8,27</td>
</tr>
<tr>
<td>3</td>
<td>15,34</td>
<td>3</td>
<td>7,26</td>
</tr>
<tr>
<td>4</td>
<td>14,33</td>
<td>4</td>
<td>6,25</td>
</tr>
<tr>
<td>5</td>
<td>13,32</td>
<td>5</td>
<td>5,24</td>
</tr>
<tr>
<td>6</td>
<td>12,31</td>
<td>6</td>
<td>4,23</td>
</tr>
<tr>
<td>7</td>
<td>11,30</td>
<td>7</td>
<td>3,22</td>
</tr>
</tbody>
</table>

Ground  2,20,21  
+12 Volts  19  
+5 Volts  1  

NOTE: The CA378 cable is designed specifically for customers upgrading from a 3093 ISA board to the 8004 PCI board. If you are not upgrading from the 3093 board, use the standard CA165 cable that ships with the 8004 PCI digital I/O board.

CA378 Output Pin Assignments (DB-37 Male)

Outputs are interfaced via the DB-37 male connector on the optional CA378 cable.

<table>
<thead>
<tr>
<th>Port C Bit</th>
<th>Relay</th>
<th>Port C Pins</th>
<th>Port D Bit</th>
<th>Relay</th>
<th>Port D Pins</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>K16</td>
<td>2,20</td>
<td>0</td>
<td>K8</td>
<td>10,28</td>
</tr>
<tr>
<td>1</td>
<td>K15</td>
<td>3,21</td>
<td>1</td>
<td>K7</td>
<td>11,29</td>
</tr>
<tr>
<td>2</td>
<td>K14</td>
<td>4,22</td>
<td>2</td>
<td>K6</td>
<td>12,30</td>
</tr>
<tr>
<td>3</td>
<td>K13</td>
<td>5,23</td>
<td>3</td>
<td>K5</td>
<td>13,31</td>
</tr>
<tr>
<td>4</td>
<td>K12</td>
<td>6,24</td>
<td>4</td>
<td>K4</td>
<td>14,32</td>
</tr>
<tr>
<td>5</td>
<td>K11</td>
<td>7,25</td>
<td>5</td>
<td>K3</td>
<td>15,33</td>
</tr>
<tr>
<td>6</td>
<td>K10</td>
<td>8,26</td>
<td>6</td>
<td>K2</td>
<td>16,34</td>
</tr>
<tr>
<td>7</td>
<td>K9</td>
<td>9,27</td>
<td>7</td>
<td>K1</td>
<td>17,35</td>
</tr>
</tbody>
</table>

Ground  18,36,37  
+ 5 Volts  19  
+ 12 Volts  1  

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Electrical Characteristics

Features
- 2 sets SPST relays with each having 8 relays
- 2 eight bit input ports
- DB-37 Male connector for relay outputs
- DB-37 Female connector for optically isolated inputs
- Highly reliable 10 VA DIP reed relays
- Multiple adapters can reside in same computer

Specifications

Input Ports
- Turn On Current: 3mA
- Isolator Diode Drop: 1.1 VDC
- Resistor Power Max: .25 W
- Maximum Input Range: 10 – 30 VDC/VAC

Output Relays
- Contact Max Power Rating: 10 W
- Contact Voltage Maximum: 100 VDC/VAC
- Contact Current Maximum: .5A AC/DC RMS
- Contact Resistance, Initial: .15 Ω
- Rated Life:
  - Low Load: 200 Million Closures
  - Maximum Load: 100 Million Closures
- Contact Speed:
  - Operate: .5 mS
  - Release: .5 mS
  - Bounce: .5 mS
- Maximum Operating Speed: 600 Hz

Temperature Range
- Operating: 0°C – 70°C
- Storage: -50°C – 105°C

Power Requirements
- +5VDC @ 450mA
- +12VDC (Optional Use)

Physical Dimensions
- PCB Length: 6.0” (15.2 cm)
- PCB Height: 4.2” (10.7 cm, including Gold fingers)

Manufacturing
All Sealevel Systems Printed Circuit boards are built to UL 94V0 rating and are 100% electrically tested. These printed circuit boards are solder mask over bare copper or solder mask over tin nickel.
Example Circuits

Input Circuit

Output Circuit
Appendix A - Troubleshooting

Following these simple steps can eliminate most common problems.

1. **Install software first.** After installing the software then proceed to adding the hardware. This places the required installation files in the correct locations.

2. Read this manual thoroughly before attempting to install the adapter in your system.

3. Use Device Manager under Windows to verify proper installation.

4. Use the SeaIO Control Panel applet or the Device Manager’s property page for card identification and configuration.

5. If these steps do not solve your problem, please call Sealevel Systems’ Technical Support, (864) 843-4343. Our technical support is free and available from 8:00AM-5:00PM Eastern Time Monday through Friday. For email support contact support@sealevel.com.
Appendix B - How To Get Assistance

Begin by reading through the Trouble Shooting Guide in Appendix A. If assistance is still needed please see below.

When calling for technical assistance, please have your user manual and current adapter settings. If possible, please have the adapter installed in a computer ready to run diagnostics.

Sealevel Systems provides an FAQ section on its web site. Please refer to this to answer many common questions. This section can be found at http://www.sealevel.com/faq.asp

Sealevel Systems maintains a Home page on the Internet. Our home page address is http://www.sealevel.com. The latest software updates, and newest manuals are available via our FTP site that can be accessed from our home page.

Technical support is available Monday to Friday from 8:00 a.m. to 5:00 p.m. eastern time. Technical support can be reached at (864) 843-4343.

RETURN AUTHORIZATION MUST BE OBTAINED FROM SEALEVEL SYSTEMS BEFORE RETURNED MERCHANDISE WILL BE ACCEPTED. AUTHORIZATION CAN BE OBTAINED BY CALLING SEALEVEL SYSTEMS AND REQUESTING A RETURN MERCHANDISE AUTHORIZATION (RMA) NUMBER.
Appendix D - Compliance Notices

Federal Communications Commission Statement

FCC - This equipment has been tested and found to comply with the limits for Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in such case the user will be required to correct the interference at the users expense.

EMC Directive Statement

Products bearing the CE Label fulfill the requirements of the EMC directive (89/336/EEC) and of the low-voltage directive (73/23/EEC) issued by the European Commission.

To obey these directives, the following European standards must be met:

EN55022 Class A - “Limits and methods of measurement of radio interference characteristics of information technology equipment”

EN55024 – “Information technology equipment Immunity characteristics Limits and methods of measurement”.

EN60950 (IEC950) - “Safety of information technology equipment, including electrical business equipment”

Warning

This is a Class A Product. In a domestic environment, this product may cause radio interference in which case the user may be required to take adequate measures to prevent or correct the interference.

Always use cabling provided with this product if possible. If no cable is provided or if an alternate cable is required, use high quality shielded cabling to maintain compliance with FCC/EMC directives.
Warranty

Sealevel's commitment to providing the best I/O solutions is reflected in the Lifetime Warranty that is standard on all Sealevel manufactured products. We are able to offer this warranty due to our control of manufacturing quality and the historically high reliability of our products in the field. Sealevel products are designed and manufactured at its Liberty, South Carolina facility, allowing direct control over product development, production, burn-in and testing.

Sealevel Systems, Inc. (hereafter "Sealevel") warrants that the Product shall conform to and perform in accordance with published technical specifications and shall be free of defects in materials and workmanship for life. In the event of failure, Sealevel will repair or replace the product at Sealevel's sole discretion. Failures resulting from misapplication or misuse of the Product, failure to adhere to any specifications or instructions, or failure resulting from neglect or abuse are not covered under this warranty.

Warranty service is obtained by delivering the Product to Sealevel and providing proof of purchase. **Return authorization must be obtained from Sealevel Systems before returned merchandise will be accepted. Authorization is obtained by calling Sealevel Systems and requesting a Return Merchandise Authorization (RMA) number.** The Customer agrees to insure the Product or assume the risk of loss or damage in transit, to prepay shipping charges to Sealevel, and to use the original shipping container or equivalent. Warranty is valid only for original purchaser and is not transferable.

Sealevel Systems assumes no liability for any damages, lost profits, lost savings or any other incidental or consequential damage resulting from the use, misuse of, or inability to use this product. Sealevel Systems will not be liable for any claim made by any other related party.

This warranty applies to Sealevel manufactured Product. Product purchased through Sealevel but manufactured by a third party will retain the original manufacturer's warranty.

Sealevel Systems, Incorporated
2779 Greenville Highway
P.O. Box 830
Liberty, SC 24857 USA
(864) 843-4343 FAX: (864) 843-3067
www.sealevel.com
email: support@sealevel.com

Technical Support is available Monday - Friday from 8 a.m. to 5 p.m. Eastern time

**Trademarks**

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